

APPLICATION FOR UNITED STATES LETTERS PATENT

SPECIFICATION

5 (Case No. 98,162-B)

Title: METHOD FOR REMOVING ORGANIC CONTAMINANTS FROM A
SEMICONDUCTOR SURFACE

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Reference to Related Applications

This application is a continuation-in-part of United States patent application Serial No. 09/022,834 filed on February 13, 1998 and claims priority benefits under 35 U.S.C. §119(e) to United States provisional application Serial No. 60/040,309, filed on February 14, 1997, to United States provisional application Serial No. 60/042,389, filed on March 25, 1997, and to United States provisional application Serial No. 60/066,261, filed on November 20, 1997.

Background of the invention

A. Field of the Invention

The present invention is related to a method for removing organic contaminants from a semiconductor surface.

The present invention is also related to the use of this method for a number of applications such as cleaning sequences or cleaning after VIA etching and other etch processes.

B. Description of Related Art

The semiconductor surface preparation prior to various processing steps such as oxidation, deposition or growth processes, has become one of the most critical issues in semiconductor technology. With the rapid approach of sub halfmicron and quarter micron design rules, very small particles and low levels of contamination or material impurities ($\sim 10^{10}$ atoms/cm² and lower) can have a drastic effect on process yields. The contaminants that are to be removed from a semiconductor surface include metallic impurities, particles and organic material. A commonly used technique to reduce foreign particulate matter contamination

level on semiconductor surfaces, is the immersion of wafers in chemical solutions.

Organic material is one of the contaminants that has to be removed from the semiconductor wafer surface. In a pre-clean stage, absorbed organic molecules prevent cleaning chemicals from contacting with the wafer surface, thus leading to non-uniform etching and cleaning on the wafer surface. In order to realize contamination free wafer surfaces, organic impurities have to be removed before other wafer cleaning processes. Traditional wet cleaning processes involve the use of sulfuric peroxide mixtures (SPM) to remove organic molecules. However, SPM uses expensive chemicals and requires high processing temperatures, and causes problems in terms of chemical waste treatment.

Other sources of organic contamination also arise during a standard IC process flow. Such sources can be photoresist layers or fluorocarbon polymer residues that are deposited on a substrate.

The fluorocarbon residues originate from the exposure of semiconductor (silicon) substrates to dry oxide etch chemistries. In conventional oxide etching with fluorocarbon gases, an amount of polymer is intentionally generated in order to achieve a vertical sidewall profile and better etch selectivity to the photoresist mask and underlying film. Etch selectivity in a SiO_2 -Si system can be achieved under certain process conditions through the formation of fluorocarbon based polymers. The polymerisation reaction occurs preferably on Si, thus forming a protective coating and etch selectivity between Si and SiO_2 . After selective etching, both resist and polymer-like residue must be removed from the surface. If the polymer is not completely removed prior to the subsequent metal deposition, the polymer will mix with sputtered metal atoms to form a high resistance

material resulting in reliability concerns. Methods of polymer removal depend on the plasma etch chemistry, plasma source and the composition of the film stack. However, for dry processes, the application of O₂ or H₂ containing gases have been applied to remove the fluorocarbon polymers. For wet cleaning techniques an amine based solvent (U.S. Patent No. 5,279,771 and U.S. Patent No. 5,308,745, both of which are hereby incorporated by reference) is frequently applied. Organic photoresist removal generally involves wet or dry oxidative chemistries (i.e. O₂ plasma, SPM) or dissolution processes based on solvent strippers. These processes are both expensive and environmentally harmful in terms of waste treatment.

In an attempt to find alternative efficient cleans for the removal of organic contamination (including photoresist and etch residues) from Si surfaces, the use of ozonated chemistries has been investigated. Ozone has been used extensively in the field of waste water treatment and drinking water sterilisation, because of its strong oxidising power. An additional benefit of ozone is its harmless residue after decomposition and/or reaction (H₂O, CO₂, O₂). It is generally presumed that oxidative action of ozone towards organic contamination involves two different oxidation pathways, either direct oxidation or advanced oxidation. Direct oxidation or ozonolysis involves molecular ozone as the prime oxidant. It predominantly occurs at carbon-carbon double bonds. This type of oxidation is favored in the low pH region of the waste water. Advanced oxidation involves secondary oxidants as the prime oxidant (e.g. OH radicals). This type of oxidation is more reactive, but less sensitive and is predominant at conditions that favor OH radical formation, such as high pH, elevated temperature, addition of enhancers (e.g. H₂O₂), UV radiation. In real life situations,

one often deals with a mixture of contaminants having a different reactivity towards ozone. However, both oxidation pathways are concurrent and conditions that favor advanced oxidation pathways will occur at the expense of the efficiency of eliminating organic contamination with higher reactivity towards molecular ozone. In order to optimize the organic removal efficiency of ozonated chemistries, it is critical to identify the parameters that influence both oxidation pathways.

In recent years, ozone was introduced in the microelectronics industry because of its strong oxidizing capabilities. When ozone gas is dissolved into water, its self-decomposition time gets shorter compared to the gaseous phase. During self-decomposition, ozone generates OH radicals as a reaction by-product, which is according to G.Alder and R.Hill in J.Am.Chem.Soc. 1950, 72 (1984), hereby incorporated by reference, believed to be the reason for decomposition of organic material.

U.S. Patent No. 5,464,480, which is hereby incorporated by reference, describes a process for removing organic material from semi-conductor wafers. The wafers are contacted with a solution of ozone and water at a temperature between 1° and 15°C. Wafers are placed into a tank containing deionized water, while diffusing ozone into the (sub-ambient) deionized water for a time sufficient to oxidize the organic material from the wafer, while maintaining the deionized water at a temperature of about 1° to about 15°C, and thereafter rinsing the wafers with deionized water. The purpose of lowering the temperature of the solution to a range between 1° and 15° C is to enable sufficiently high ozone concentrations into water to oxidize all of the organic material onto the wafer into insoluble gases.

European Patent Application EP-A-0548596 describes a spray-tool process, whereby during the cleaning process, various liquid chemicals, ultra-pure water or a mixed phase fluid comprising an ozone-containing gas and
5 ultra pure water are sprayed onto substrates or semiconductor wafers in a treating chamber filled with ozone gas. Rotation is necessary to constantly renew thin films of treating solution and promoting removal of undesired materials by means of centrifugal force.

10 U.S. Patent No. 5,181,985, which is hereby incorporated by reference, describes a process for the wet-chemical surface treatment of semiconductor wafers in which aqueous phases containing one or more chemically active substances in solution act on the wafer surface, with water
15 in a finely divided liquid state such as a mist. The process consists of spraying the water mist over the wafer surface and then introducing chemically active substance in the gaseous state so that these gaseous substances are combined with the water mist in order to have an interaction of the
20 gas phase and the liquid phase taking place on the surface of the semiconductor wafers. The chemical active substance are selected from the group consisting of gases of ammonia, hydrogen chloride, hydrogen fluoride, ozone, ozonized oxygen, chlorine and bromine. The water is introduced into the
25 system at a temperature of 10°C to 90°C.

U.S. Patent No. 5,503,708, which is hereby incorporated by reference, describes a method and an apparatus for removing an organic film wherein a mixed gas including an alcohol and one of ozone gas and an ozone-
30 containing gas is supplied into the processing chamber at least for a period before that the semiconductor wafer is placed in said processing chamber, so that the mixed gas will

act on the organic film formed on the surface of the semiconductor wafer.

Document JP-A-61004232 describes a cleaning method of semiconductor substrates. The method is presented
5 as an alternative for traditional acid-hydrogen peroxide cleans, which in the prior art are used for heavy metal reduction on silicon wafers. Substrates are dipped in a solution of an undiluted organic acid, e.g. formic acid or acetic acid filled into a cleaning tank wherein ozone or
10 oxygen is supplied from the bottom of the tank so as to bubble into the solution, said solution being heated to a temperature comprised between 100°C to 150°C. Organic waste matter is oxidized by means of the ozone and can be dissolved and removed. In other words, this Japanese publication
15 describes cleaning of heavy metals on semiconductor wafers through formation of metal formate or metal acetate compounds and of dissolving the organic waste matter from semiconductor wafers by means of ozone.

Summary of the present invention

The present invention aims to suggest an improved method for the removal of organic contaminants from a semiconductor substrate.

5 More particularly, the present invention aims to suggest a method of removal of organic contamination such as photoresist, photoresidue, dry etched residue which can occur in any process step of the fabrication of a semiconductor substrate.

10 As a first aspect, the present invention is related to a method of removing organic contaminants from a substrate comprising the steps of holding said substrate in a tank, and filling said tank with a gas mixture comprising water, ozone and an additive acting as a scavenger. The term
15 tank for the purpose of this and related patent applications is meant to cover any kind of tool or reaction chamber wherein substrates are held for the purpose of cleaning or removing organic contamination. Thus the term tank is to cover tools or reaction chambers known in the art such as wet
20 benches, vessels, spray processors, spinning tools, single tank and single wafer cleaning tools.

As a second aspect, the present invention is related to a method for removing organic contaminants from a substrate, comprising the steps of:

25 holding said substrate in a tank;
filling said tank with a liquid comprising water, ozone and an additive acting as a scavenger; and
maintaining said liquid at a temperature less than the boiling point of said liquid.

As a third aspect, the present invention is related to a method for removing organic contaminants from a substrate comprising the steps of:

holding said substrate in tank;

5 filling said tank with a fluid comprising water, ozone and an additive acting as a scavenger, and wherein the proportion of said additive in said fluid is less than 1% molar weight of said fluid.

By scavenger, it is meant a substance added to
10 a mixture or any other systems such as liquid, gas, solution in order to counteract the unwanted effects of other constituents of the mixture or system.

Said additive should preferably act as OH radical scavengers. A radical is an uncharged species (i.e.,
15 an atom or a di-atomic or poly-atomic molecule) which possesses at least one unpaired electron. Examples of scavenger can be carboxylic or phosphonic acid or salts thereof such as acetic acid (CH_3COOH), and acetate (CH_3COO^-) as well as carbonate ($\text{H}_x\text{CO}_3^{-(2-x)}$) or phosphate ($\text{H}_x\text{PO}_4^{-(3-x)}$).

20 In a fourth aspect of the present invention, the silicon oxidizing capabilities of mixtures comprising ozone and DI-water are exploited. The fourth aspect of the invention is related to an efficient cleaning of the surface of a silicon wafer which can be achieved through a sequence
25 of steps as:

Step 1 : an oxide growth on the silicon surface;

Step 2 : oxide removal;

Step 3 (optional) : growth of a thin passivating oxide layer for applications wherein a hydrophilic surface is preferred;

30 Step 4 : drying of the silicon wafer.

The different steps can be executed as follows:

Step 1 : an oxide growth on the silicon surface can be executed through the silicon oxidizing activity of a fluid (liquid, gas, steam, vapor or a mixture thereof) mixture of ozone and water. The fluid can further comprise an additive
5 such as a scavenger.

Step 2 : the oxide removal step can be executed in a diluted HF-clean with or without additives such as HCl.

Step 3 (optional) : the growth of a thin passivating oxide layer for applications wherein a hydrophilic surface is
10 preferred can be executed in ozonized mixtures such as dilute HCl/ozone mixtures.

Step 4 : drying of the silicon wafer can be achieved through a Marangoni-type drying or drying step accompanied by heating of the silicon wafers.

15 This sequence of steps can be executed in any kind of reaction chamber or tank such as a wet bench, a single tank, a spray processor or a single-wafer cleaning tool.

20 The invention can be used in the fabrication of silicon wafers for Integrated Circuits. The invention can also be used in related fields, like the fabrication of flat panel displays, solar cells, or in micro-machining applications or in other fields wherein organic contaminants
25 have to be removed from substrates.

Brief description of the drawings

Figure 1 is a schematic representation of a deep VIA etch structure.

30 **Figure 2** is a schematic representation of an Al overetched VIA structure.

Figure 3 is a representation of the experimental set-up used in the moist gas phase processing.

Figure 4 is representing a SEM micrograph of via structure prior to any cleaning treatment.

5 **Figure 5** represents a SEM micrograph of a VIA structure after 45' O2 dry strip.

10 **Figure 6** represents a SEM micrograph of a deep VIA as represented in figure 1 after 10' exposure to a preferred embodiment of the method of the present invention.

Figure 7 represents an SEM micrograph of Al overetched via according to figure 2 after 10' exposure to a preferred embodiment of the method of the present invention.

15 **Figure 8** is representing an ozone bubble immersion experimental set-up of the liquid phase processing.

20 **Figure 9** represents the resist removal process efficiency number (nm removal / process time * ozone concentration) for positive and negative resist removal as a function of the acetic acid concentration.

Figure 10 represents the main parameter effects on resist removal rate (nm removal / process time) for positive resist removal.

25 **Figure 11** represents the main parameter effects on resist removal process efficiency number (nm removal / process time * ozone concentration) for positive resist removal (with 95% confidence levels).

Figure 12 represents the resist removal efficiency as a function of the temperature and the ozone concentration in a static system.

Figure 13 represents the resist removal efficiency as a function of the temperature and ozone concentration in bubble or moist gasphase processing.

Figure 14 represents a possible scheme of reactions in an aqueous ozone.

Figure 15 represents the effect of OH radical scavenging on ozone concentration in an overflow tank.

Figure 16 represents the effect of repeated addition of hydrogen peroxide (H_2O_2 at 0.17 mmol/l at $t = 0, 13, 20, 24$ minutes) to a de-ionised water solution spiked with 0.23 mmol/l of acetic acid.

Detailed description of several embodiments of the present invention

The purpose of the present invention is related to a method for removing organic contamination from a substrate and/or to a method for oxidizing a silicon wafer. Said substrate can be a semiconductor surface. Said method can be applied for the removal of photoresist and organic post-etch residues from silicon surfaces. Said organic contamination can be a confined layer covering at least part of said substrate. Said confined layer can have a thickness in a range of submonolayer coverage to 1 μm . Said method is applicable for either gasphase or liquid processes.

In the following specification, a first preferred embodiment of the invention for gas phase

processing and a second preferred embodiment for liquid phase processing are described.

Description of a first preferred embodiment for gasphase processing

In said gasphase process, said substrates are placed in a tank such that said substrates are in contact with a gas mixture containing water vapor, ozone and an additive acting as a scavenger.

Said scavenger is a substance added to said mixture to counteract the unwanted effects of other constituents. Said scavenger typically acts as an OH radical scavenger. Said additive can be a carboxylic or a phosphonic acid or salts thereof. More preferably, said additive is acetic acid.

The proportion of said additive in said gas mixture is preferably less than 10% molar weight of said gas mixture. The proportion of said additive in said gas mixture is more preferably less than 1% molar weight of said gas mixture. Even more preferably, the proportion of said additive in said gas mixture is less than 0.5% molar weight of said gas mixture. Even more preferably, the proportion of said additive in said gas mixture is less than 0.1% molar weight of said gas mixture.

Said gas mixture can also contain oxygen, nitrogen, argon or any other inert gas. The ozone concentration of said gas mixture is typically below 10 - 15% molar weight. The operational temperature of said mixture is below 150°C and preferably higher than the temperature of

said substrate. The water vapor can be typically saturated at the operational temperature of said mixture.

Said method also comprises a step of rinsing said substrate with a solution. Said rinsing solution
5 comprises preferably de-ionized water. Said rinsing solution can further comprise HCl and/or HF and/or HNO₃ and/or CO₂ and/or O₃. Said rinsing solution can also be subjected to megasonic agitation.

According to a preferred embodiment, the method
10 can also comprise the step of filling said tank with a liquid or a solution comprising essentially water and said additive, the solution level in said tank remaining below the substrate and wherein said solution is heated. Said tank is then filled with a water vapor containing said additive. Said tank is
15 further filled with ozone. According to a preferred embodiment, the ozone can be bubbled through said solution. Preferably, said solution is heated in a range between 16°C and 99°C and even more preferably between 20°C and 90°C. Even more preferably, the solution is heated between 60°C and
20 80°C.

According to the best mode embodiment, the set-up denoted as moist ozone gasphase process uses a quartz container filled with only a minute amount of solution or liquid, sufficient to fully immerse a O₃ diffuser. The
25 solution is DI water, spiked with an additive, such as acetic acid. A lid is put on the quartz container. The liquid is heated to 80°C. Wafers are placed above the solution interface but are not immersed. The ozone diffuser is fabricated from fused silica, and the ozone generator

5 (Sorbius) is operated with an oxygen flow which maximizes the
ozone content in the gas flow. In the best mode embodiment, a
flow of 3 l/min O₂ is used. At all time the ozone is bubbled
directly into the solution (no bubble reduction) throughout
the experiment. Heating of the solution in a sealed container
and continuous O₃ bubbling through the solution exposes the
wafers to a moist O₃ ambient. The operational temperature is
80°C, while the DI water is acidified (1/100 volume ratio)
with acetic acid. Wafers are to be processed sufficiently
10 long and a rinse step follows the moist gas phase treatment.
In an embodiment, wafers are processed for 10 minutes, and
subsequently rinsed in DI water for 10 minutes.

15 In another embodiment of the invention, the
ozone gas is bubbled by an O₃ diffuser fully immersed in a
static quartz bath containing DI-water spiked with acetic
acid (pH~1, preferably 100:1 dilution of 16M CH₃COOH) or
nitric acid (pH~1.5, preferably 100:1 dilution of 16M HNO₃).
A lid is put on the quartz container. The wafers are placed
above the liquid or solution to be exposed to a moist O₃
20 ambient for 10 minutes at 50° or 80°C.

Yet in another embodiment of the invention, a
cleaning procedure involving 10 minute combination of
successive steps of moist O₃ gas phase process at 80°C for 10
min and an acid rinse with 5% H₂SO₄ in H₂O₂ at 90°C is done.

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Description of a second preferred embodiment for liquid
processing

In said liquid process, said substrates are
placed in a tank such that said substrates are in contact

with a liquid or solution mixture comprising water, ozone and an additive acting as a scavenger. Said scavenger is a substance added to said mixture to counteract the unwanted effects of other constituents. Said scavenger typically acts
5 as an OH radical scavenger.

Said additive can be a carboxylic or a phosphonic acid or salts thereof, preferably said additive is acetic acid. The proportion of said additive in said liquid is less than 1% molar weight of said liquid. Preferably, the
10 proportion of said additive in said liquid is less than 0.5 molar weight of said liquid. More preferably, the proportion of said additive in said liquid or solution is less than 0.1% molar weight of said liquid.

Said liquid can also be subjected to megasone
15 agitation.

According to a preferred embodiment, the method also comprises a step of maintaining said liquid at a temperature less than the boiling point of said liquid or a solution. Preferably, the temperature of said liquid is
20 lower than 100°C. More preferably, the temperature of said liquid is comprised between 16°C and 99°C. More preferably, the temperature of said liquid is comprised between 20°C and 90°C. Even more preferably, the temperature of said liquid is comprised between 60°C and 80°C.

Preferably, the ozone is bubbled through said
25 liquid or solution which allows a contact of the bubbles of ozone with the substrates.

Yet according to another preferred embodiment, said method also comprises a step of rinsing said substrate

with a rinsing solution. Preferably, said rinsing solution comprises de-ionized water. More preferably, said rinsing solution further comprises HCl and/or HF and/or HNO₃ and/or CO₂ and/or O₃. Said rinsing solution or liquid can also be
5 subjected to megasonic agitation.

According to the best mode embodiment of the invention, the following set-up is used: The O₃ set-up (immersion based), denoted as bubble experiment, consists of a quartz container holding 7 litres of a liquid and an ozone
10 diffuser located at the bottom of the tank. The liquid can be heated. Operational temperature is 45°C. The ozone diffuser is fabricated from fused silica, and the ozone generator (Sorbus) is operated with an oxygen flow which maximizes the ozone content in the gas flow. In the best mode
15 embodiment, a flow of 3 l/min O₂ is used. At all time the ozone is bubbled directly into the quartz tank (no bubble reduction) throughout the experiment. The substrates are positioned directly above the ozone diffuser, and immersed in the liquid. As such O₂/O₃ bubbles contact the surface. The
20 substrates are exposed to an ozone treatment with varying acetic acid concentrations in the bubble set-up. The substrates are exposed to an ozone clean between 0-11,5 mol/l (0, 0.1ml (0.46mmol/l), 1.0ml (2.3mmol/l) and 5.0ml (11.5mmol/l)) of acetic acid added to the 7 liter of DI
25 water.

According to another preferred embodiment of the invention, conventional reaction chambers are used to permit water, including a scavenger, gaseous chemically active substances, and the surface of the semiconductor
30 wafers to interact with each other. Examples of such conventional reaction chambers are those offered for sale by the companies F.S.I. and SEMITool and STEAG. When such reaction chambers are used, an individual semiconductor

wafer, or a plurality of semiconductor wafers can be introduced to a working position. It is then possible to control the supply of the water and of finely divided water and/or gaseous, chemically active substances and their
5 uniform action on the wafer surfaces. The liquids produced in the process can also be collected and removed. The wafers can also easily be removed after treatment and, if necessary, a further batch can be introduced. Facilities may also be provided to agitate the wafers in the working position, for
10 example by rotation. Suitable reaction chambers may be designed similar to, or based on, the conventional wet benches or the spray etching or spray cleaning chambers, also referred to as spray processors. Suitable devices to supply the various gases and the water may be advantageously
15 provided, instead of the introduction facilities for the various solutions. In principle, it is also possible to operate mixed systems which have both the facility for introducing gases and also solutions. It is possible to spray water into the reaction chambers using a nozzle system
20 to provide a homogeneous, aerosol-like spray mist in the interior space of the chamber. The mist consists thus of finely dispersed liquid droplets. It is also possible to spray a treating solution onto the undesired materials on the substrate that is rotating in a treating chamber filled with
25 an ozone-containing gas atmosphere.

The treating solution used in the method of the invention, for instance, may be various liquid chemicals, ultra-pure water comprising a scavenger, and a mixed phase liquid comprising an ozone-containing gas and ultra-pure
30 water. This embodiment is also directed to an apparatus for treating substrates, which comprises a closed treating chamber with a substrate holder located therein, in which a plurality of substrates are placed, said substrate holder

being attached to the treating chamber coupled to a rotary shaft or a rotary table coupled to a rotary shaft and being provided with a nozzle for feeding an ozone-containing gas or a treating solution or a nozzle for feeding a mixed phase
5 fluid comprising an ozone-containing gas and a treating solution.

More specifically, the embodiment is designed such that when various liquid chemicals, ultra-pure water and a scavenger or a mixed phase fluid comprising an ozone-
10 containing gas and ultra-pure water and a scavenger are sprayed onto undesired materials on substrates in a treating chamber having its ozone concentration regulated to a certain or higher level by feeding thereto an ozone-containing gas or a mixed phase fluid comprising an ozone-containing gas and
15 ultra-pure water, the substrates with the undesired materials thereon are rotated to constantly renew thin films of the treating solution on the surfaces of the substrates by means of centrifugal force, thereby promoting removal of the undesired materials.

Rotating the substrates at high speed produces large enough effects, because the thickness of the films of ultra-pure water formed on the surfaces of the substrates are very thin and the films of ultra-pure water formed on the surfaces of the substrates are continuously renewed. Heating
20 the liquid also has large enough effects.

The present invention is also related to specific applications of the method as described in the two preferred embodiments of the present invention.

30 Application 1: VIA CLEANING

The method of the present invention can be applied for wafer cleaning technologies after plasma etching processes especially into submicron processes. Dry etching of

silicon and its compounds is based on the reaction with fluorine, with resulting fluorocarbon polymer contamination. The fluorocarbon residues originate from the exposure of semiconductor (silicon) substrates to dry oxide etch chemistries. In conventional oxide etching with fluorocarbon gases, an amount of polymer is intentionally generated in order to achieve a vertical sidewall profile and better etch selectivity to the photoresist mask and underlying film. Etch selectivity in a SiO₂-Si system can be achieved under certain process conditions through the formation of fluorocarbon based polymers.

The polymerisation reaction occurs preferably on Si, thus forming a protective coating and etch selectivity between Si and SiO₂. After selective etching, both resist and polymer-like residue must be removed from the surface. If the polymer is not completely removed prior to the subsequent metal deposition, the polymer will mix with sputtered metal atoms to form a high resistance material resulting in reliability concerns. Methods of polymer removal depend on the plasma etch chemistry, plasma source and the composition of the film stack. However, for dry processes, O₂ or H₂ containing gases have been applied to remove the fluorocarbon polymers. For wet cleaning techniques an amine based solvent U.S. Patent No. 5,279,771 and U.S. Patent No. 5,308,745 is frequently applied. These processes are frequently both expensive and environmentally harmful in terms of waste treatment.

Figures 1 and 2 (both figures not drawn to scale) show different VIA test structures prepared on p-type wafers. The first structure consists of 500 nm oxide, 30/80 nm Ti/TiN, 700 nm AlSiCu, 20/60 nm Ti/TiN, 250 nm oxide, 400 nm SOG and 500 nm oxide (starting from the silicon substrate). The second structure contains the following

layers; 500 nm oxide, 30/80 nm Ti/TiN, 700 nm AlSiCu, 20/60 nm Ti/TiN and 500 nm oxide (also starting from the silicon substrate). Subsequently, these structures are coated with I-line resist and exposed through a mask set with contact holes ranging from 0.4 μ m till 0.8 μ m in diameter. VIA's were etched in a CF₄/CHF₃ plasma. For the first set of wafers VIA's are etched through the 500 nm oxide / 400 nm SOG / 250 nm oxide, stopping on TiTiN/Al, for the second set of wafers, VIA's are overetched through the 500 nm oxide layer into the TiTiN/Al layers. Wafers are exposed to the ozone clean directly (i.e. with resist layer and sidewall polymers on the wafer).

The set-up used for this application is represented in Figure 3. The set-up denoted as moist ozone gasphase process uses a quartz container filled with only a minute amount of liquid, sufficient to fully immerse an O₃ diffuser. The liquid is DI water, spiked with an additive, such as acetic acid. A lid is put on the quartz container. The liquid is heated to 80°C. Wafers are placed directly above the liquid interface but are not immersed. The ozone diffuser is fabricated from fused silica, and the Sorbus generator is operated with a flow of 3 l/min O₂ flow. At all time the ozone is bubbled directly into the quartz tank (no bubble reduction) throughout the experiment. Heating of the liquid in a sealed container and continuous O₃ bubbling through the liquid exposes the wafers to a moist O₃ ambient. In the gasphase experiment, operational temperature was 80°C, while the DI water is acidified (1/100 volume ratio) with acetic acid. In all cases, wafers are processed for 10 minutes, and subsequently rinsed in DI water for 10 minutes.

Cleaning efficiency is evaluated from SEM measurements (on 0.6 μ m VIA's). For reference, wafers were also dry stripped for 45 minutes during an O₂ plasma treatment (i.e. leaving sidewall polymers on the wafer).

Figure 4 shows SEM micrograph of VIA structures (Figure 1) prior to exposure to any cleaning treatment, i.e. with resist and side-wall polymers present. Figure 5 is a SEM micrograph of VIA structure in Figure 1 after 45 minutes O2 dry strip. SEM micrographs for both structures in Figure 1 and 2, after 10 minutes exposure to the optimized moist ozone gasphase process with acetic acid addition, are shown in Figure 6 and 7 respectively.

It can be seen immediately that after 45 minutes O2 dry strip treatment, side wall polymers are still clearly visible. However, if we consider the gasphase experiment, we do observe an excellent cleaning efficiency (Figure 6 and 7). In the gasphase experiment, resist coating as well as sidewall post-etch polymer residues are no longer observed on the surface.

Moist ozone gasphase treatment with acetic acid spiking has been demonstrated to be efficient in removing both resist layers and sidewall polymer residues from VIA-etched wafers. This is due to both physical and chemical enhancement of the ozone efficiency for removal of organic contamination.

Application 2: Resist removal

As claimed hereabove, chemical additives such as acetic acid can have impact on the removal efficiency of organic contamination by means of ozonated chemistries. For this purpose, wafers coated with a resist layer are exposed to various ozonated DI water mixtures. The resist removal efficiency is evaluated. Wafers are coated with positive (IX500el from JSR electronics) and negative (UVNF from Shipley) resist. The resist covered wafers are given a DUV bake treatment to harden the resist prior to use. Also implanted wafers (5e13at/cm2 P) with positive resist are

processed. Resist thickness is monitored ellipsometrically before and after the process.

The O₃ reference set-up (immersion based) used for another specific application denoted as bubble experiment is represented in Figure 8, consists of a quartz container holding 7 litres of a liquid and an ozone diffuser located at the bottom of the tank. The liquid can be heated. Operational temperature is 45°C. The ozone diffuser is fabricated from fused silica, and the Sorbus generator is operated with a flow of 3 l/min O₂ flow. At all time the ozone is bubbled directly into the quartz tank (no bubble reduction) throughout the experiment. Wafers are positioned directly above the ozone diffuser, and immersed in the liquid. As such O₂/O₃ bubbles contact the surface, the wafers are exposed to an ozone treatment with varying acetic acid concentrations in the bubble set-up shown in Figure 7. The unimplanted resist wafers are exposed to an ozone clean with 0, 0.1ml (0.46mmol/l), 1.0ml (2.3mmol/l) and 5.0ml (11.5mmol/l) of acetic acid added to the 7 liter of DI water. The implanted wafers are exposed to cleans with either 0 or 11.5 mmol/l of acetic acid added.

For implanted resist, removal efficiency is increased by about 50% (60nm/min versus 90nm/min) upon addition of the indicated quantity of acetic acid. Results for unimplanted resist are presented in Figure 9. A process efficiency number is defined, i.e. the resist removal efficiency normalized versus ozone concentration, and expressed as a removal rate per unit of process time. The as such defined process efficiency number increases from 0.8 till 1.2 nm/(min*ppm) for negative resist and from 4.5 till 8.5 nm/(min*ppm) for positive resist. Despite the order of magnitude difference for positive and negative resist removal, general trends are identical. It can be seen that a

positive effect on the process efficiency number is generated from acetic acid addition.

Application 3: Resist removal

5 Based on the above, experimentally designed trials are done. Effect under study is the resist removal efficiency by means of ozonated chemistries, with the use of chemical additives. Both positive and negative postbaked resist are studied. The O₃ reference set-up (immersion
10 based), denoted as bubble experiment and presented in Figure 8 is used. In order to have a better assessment of the effect of the individual variables under evaluation, wafers were not exposed directly to the ozone bubbles. This lower ozone availability (no bubble or gas contact) is reflected in the
15 lower removal rate and process efficiency number compared to application 2. Variables under consideration are acetic acid, hydrogenperoxide and ozone (by varying the oxygen flow) concentration, as well as temperature and pH of the solution. The effect of pH (varied between 2 and 5, HNO₃ addition) is
20 included to determine whether or not the impact of acetic acid is not induced by the changing pH. Hydrogenperoxide is added as it is a known OH radical generator. Quantities added are 0, 0.1 or 0.2 ml (Ashland, GB, 30%). Acetic acid (Baker, reagent grade, 99%) addition is either 0, 0.5 or 1 ml in 7
25 liter of DI water. Temperature was varied between 21 and 40°C, while O₃ concentration was controlled from the O₂ flow through the generator. Low flow is 3 l/min, high flow is 5 l/min. Both for positive and negative resist removal, results are expressed as resist removal rate per unit of time.
30 Experimental results are presented in Table I. RS/Discover is used to analyse the experimental results. This is done using a stepwise multiple regression according to a least squares

method and a quadratic model. This model accounts for about 90% of the variation observed in the experimental results.

Only results for positive resist are presented in Figures 10 and 11, the statistics for negative resist removal are identical. The main effects on all of the responses is shown in Figure 10. Notice that the largest positive effect on resist removal is due to the change in acetic acid concentration (going from 0 till 715 μ l HAC addition), with pH being of far less importance. Also, the resist removal rate is reduced by the addition of hydrogenperoxide (going from 0 till 200 μ l). From this graph it could be concluded that the temperature is of little importance. However, the ozone concentration is strongly dependent on the temperature (solubility and stability relate inversely with temperature), which biases the results. Therefore, a process efficiency number is defined; i.e. the resist removal efficiency normalized versus ozone concentration and expressed as a removal rate per unit of time and per unit of ozone (i.e. nm / (min * ppm)). The as such obtained process efficiency number varies between 0.2 and 4nm/(min * ppm) for positive resist and 0.03 and 0.4 nm/(min * ppm) for negative resist. The outcome of the impact of the various parameters on the process efficiency number is plotted in Figure 11 for positive resist removal. Despite the order of magnitude difference between positive and negative resist removal, general trends are identical. It can be seen that a positive effect on the process efficiency number is generated from acetic acid addition, ozone concentration and temperature enhancement.

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Application 4 : Resist removal

In a further study of the method of the present invention, another experiment is described hereunder.

The main requirement for the ozonated chemistries is fast and complete removal of organic contaminants (e.g. clean room air components, photoresist or side-wall polymers). Critical parameters influencing the removal efficiency are to be identified. However, also other parameters such as ozone concentration and temperature are likely important. Therefore, the impact of O_3 concentration and operational temperature for positive resist removal efficiency was evaluated experimentally. Wafers coated with a 5 nm thick photoresist coating were prepared and immersed in a static bath containing DI water (set-up as in Figure 8, but ozone bubbling off during immersion). Ozone concentration was varied between 0 and 12 ppm, and temperature between 20, 45 and 70°C. Purposely, 1 min cleans are done in static conditions (i.e. gas flow off, after O_3 saturation of DI), to assess the parameter impact. Principal results are shown in Fig. 5, where cleaning efficiency is plotted versus O_3 concentration for the three different temperature ranges. Removal is only 50% due to the small processing time and static conditions (limited ozone availability). It can be seen that cleaning efficiency per unit of ozone, is more performing at elevated temperatures, while total removal in the time frame studied is more performing at higher ozone concentration. However, O_3 solubility decreases with temperature, while process performance increases with temperature.

Ozone concentration in solution, and thus oxidizing capabilities and cleaning performance can be maximized relying on physical aspects. One process, described

previously in U.S. Patent No. 5,464,480 operates the water at reduced temperature (chilled), in order to increase ozone solubility. Disadvantages are the lowered reactivity and longer process times due to reaction kinetics. Another possibility to improve the ozone concentration is using more efficient ozone generators and/or ozone diffuser systems to transfer ozone into the DI water. From the above observations however, it is believed that any optimized process should aim at maximizing the O_3 concentration at operating temperatures. This assumption is demonstrated with the set-ups shown in Fig. 2 and 8, where both traditional immersion with bubble contact (at subambient, ambient and elevated temperatures) a moist gasphase process (at elevated temperature) are presented. Description of both set-ups is given above. Positive resist wafers (1.2nm) are exposed for 10 min, at various temperatures (bubble), or at 80°C (gasphase). Results are shown in Figure 13. Dissolved O_3 concentration for bubble experiment (bar graph) and cleaning efficiency (line graph and cross) is shown. The cleaning behavior for the bubble experiment is understood from a process limited by kinetic factors in the low temperature range and by ozone solubility in the higher temperature range. The latter limitation is reduced for the moist ozone ambient experiment. By exposing the wafer to a moist atmosphere, a thin condensation layer is formed on the wafer. The O_3 gas ambient maintains a continuous high supply of O_3 (wt% O_3 in gas, ppm in solution). Also, the thin condensation layer reduces the diffusion limitation and allows the shortlived reactive O_3 components to reach the wafer surface, resulting in near 100% removal.

Important to note is the fact that the gasphase process, in the absence of moist is unsuccessful.

Application 5 : first step in a cleaning sequence

- 5 Yet in another application of the present invention, the silicon oxidizing capabilities of mixtures comprising ozone and DI-water are exploited. It is known in the art that an efficient cleaning of the surface of a silicon wafer can be achieved through a sequence of steps as:
- 10 Step 1 : an oxide growth on the silicon surface;
Step 2 : oxide removal;
Step 3 (optional) : growth of a thin passivating oxide layer for applications wherein a hydrophilic surface is preferred;
Step 4 : drying of the silicon wafer.
- 15 Such sequence of steps in detail is explained in the publication : "New Wet Cleaning Strategies for obtaining highly Reliable Thin Oxides" by M. Heyns et al. , Mat. Res. Soc. Symp. Proc. Vol. 315 , p. 35 (1993). It was shown in several other publications that such sequence of steps leads
- 20 to a very high particle removal efficiencies and low metallic contamination levels.

The different steps can be executed as follows :

- Step 1 : an oxide growth on the silicon surface can be
- 25 executed through the silicon oxidizing activity of a fluid (liquid or gas or vapor or steam) mixture of ozone and water. The fluid can further comprise an additive such as a scavenger.
- Step 2 : the oxide removal step can be executed in a diluted
- 30 HF-clean with or without additives such as HCl.
- Step 3 (optional) : the growth of a thin passivating oxide layer for applications wherein a hydrophilic surface is

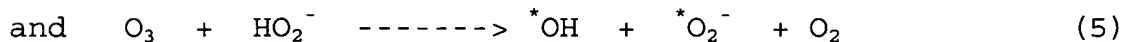
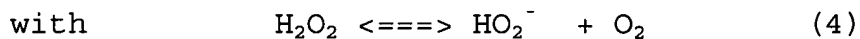
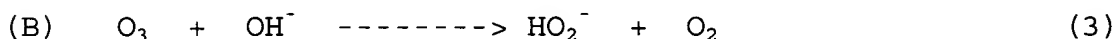
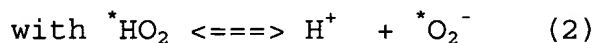
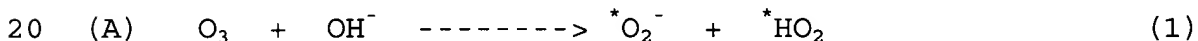
preferred can be executed in ozonized mixtures such as dilute HCl/ozone mixtures or the mixture of ozone and water.

Step 4 : drying of the silicon wafer can be achieved through a Marangoni-type drying or drying step accompanied by heating of the silicon wafers.

This sequence of steps can be executed in any kind of reaction chamber or tank such as a wet bench, a single tank, a spray processor or a single-wafer cleaning tool.

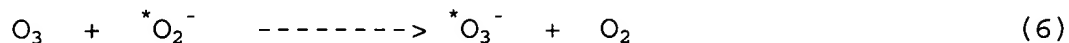
Ozone chemistry consideration

According to another plausible explanation the results obtained by using embodiments of the present invention involving ozone in aqueous solution are explained. Ozone decomposition in aqueous solutions is base catalyzed following either a radical (A) or ionic initiation mechanism (B).

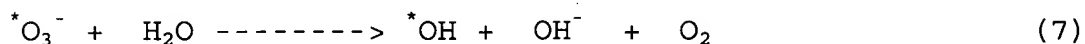


Further ozone decomposition occurs along reactions (6) and (7), independent of either type of initiation reaction. It can also be seen that despite the

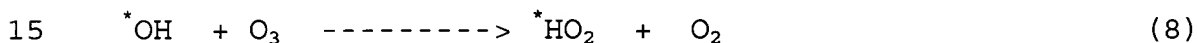
initiation mechanism, either ionic or radical, at least three ozone molecules decompose per unit of hydroxyl ions.



5



In addition to the above described ozone decomposition pathways, also the OH radicals (as formed in reaction (5) and (7)), initiate further ozone decomposition according to reaction pathway (8). Also, a chain type reaction is initiated if the reaction products are combined with reaction (2), (6) and (7).



These decomposition mechanisms are a good model to explain the observed ozone depletion in neutral or caustic aqueous environment. However, in acid environment, the observed decomposition rate of ozone is faster than can be expected from the hydroxyl concentration, given reactions (1-4). Therefore, an additional decomposition mechanism is required. This initiation mechanism is presented in equations (9-11), in combination with the earlier described reactions (2), (6) and (7).



30

Reactions (1-10) describe the depletion of ozone in aqueous environment. However, in the presence of oxidizable components the situation becomes even more

complex, and an overall picture is graphically presented in Figure 14. Transfer of ozone into aqueous solution is limited by the solubility, thus resulting in ozone loss through purging. The primary reaction is the consumption of ozone by solutes M that become oxidized. Among these reactions is also the oxidation of water to hydrogenperoxide (with resulting equilibrium $\text{H}_2\text{O}_2 \rightleftharpoons \text{HO}_2^- + \text{H}^+$). This primary reaction is often slow, therefore ozone is likely to decompose via alternative reaction pathways. As such, reaction between initiators I (OH^- , HO_2^- , ...) and ozone results in the formation of primary radicals ($^*\text{OH}$), which may either become scavenged or react further with ozone to yield more free radicals or take part in the advanced oxidation pathway of solutes M. Referring to reactions (1-10) and Figure 14, it is anticipated that the ozone chemistry can also be controlled chemically, i.e. from selective addition of additives.

The influence of additives on the ozone chemistry as derived from the above, is demonstrated for an overflow bath whereby ozone/water mixtures are prepared in a Gore ozone module (membrane based type mixer) to reduce the presence of O_2/O_3 gas bubbles in the overflow bath. Water flow in the overflow bath (20 l/min), O_2 flow (2 l/min) through the ozone generator and pressure in the ozone module (1 bar) determine the achievable O_3 levels in the bath. These variables are kept constant at the indicated values for the experiments presented here. At all times the ozone level in DI water is allowed to saturate prior to the addition of any chemical. All chemicals used are Ashland GB grade apart acetic acid (99%) which is Baker reagent grade. To eliminate the influence of reaction kinetics, all experiments are performed at room temperature. An Orbisphere labs MOCA electrochemical ozone sensor is used for all ozone measurements.

As represented in Figure 15, the behavior of acetic acid on the ozone concentration in DI water in an overflow tank is considered by adding 10 ml acetic acid (99w%) to the DI water after saturation of the ozone level. Almost immediately, the ozone level starts to increase.

Influence of acetic acid on the resist removal efficiency of ozonated chemistries.

Advanced oxidation processes rely on the presence of OH radicals which are the chain propagating radical in O₃ decomposition (K. Sehested, H. Corfitzen, J. Holcman, E.Hart, J.Phys.Chem., 1992, 96, 1005-9, which is hereby incorporated by reference). According to G.Alder and R.Hill in J.Am.Chem.Soc. 1950, 72, (1984), which is hereby incorporated by reference, OH radicals are the main reason for decomposition of organic material. Commonly applied procedures in waste water treatment processes involve, for example, UV radiation, pH or addition of hydrogenperoxide. As such, enhancement of OH radical formation is achieved.

Three different experiments using first a hydrogen peroxide, hydrogen peroxide added to acetic acid, and finally acetic acid alone are performed.

The effect of hydrogen peroxide spiking into the ozonated DI water on the removal efficiency of positive resist from silicon wafers can be seen in Table II. It should be noted that the concentration of hydrogen peroxide spiked is in the order of the actual ozone concentration in the DI water. It can be observed that spiking of a 50µl (Ashland GB, 30%) of H₂O₂ into an 7.5 l tank (0.08 mmol/l) has a strong effect. The measured resist removal rate decreases by a factor of four. Further addition of H₂O₂ reduces the resist removal efficiency even further, until the removal process becomes practically unexisting (2 nm/min removal rate). This

is contrary to the effects seen for waste water treatment, where enhanced OH radical availability results in improved removal rates for organic contamination. The organics to be removed in wastewater treatment are dispersed in the solution (as is ozone and OH radicals), while for our purposes, the organic contamination is confined in a layer covering at least part of the substrate. It is likely that for our purposes, not the total amount of 'ozone and ozone based components' that is available in the solution, but rather the chemical activity that emerges in the vicinity of the confined layer of organic material near the wafer surface is of importance.

Therefore, in this application, the OH radical catalyzed ozone decomposition mechanism is controlled through scavenging of the OH radicals formed. A scavenger is a substance added to a mixture or other system to counteract the unwanted effects of other constituents. Acetic acid or acetate is a stabilizer of aqueous ozone solutions. In Figure 16, the combined effect of acetic acid and repeated hydrogenperoxide spiking (OH radical enhancer) on ozone concentration is demonstrated. Despite the spiking of H_2O_2 at time $t=0$ (0.17mmol/l), the ozone concentration does increase slightly further in case the DI water is stabilized with only 0.23mmol/l of acetic acid. Even after several H_2O_2 additions (each time 0.17mmol/l), the ozone level did not drop below the initial starting level. This confirms the robustness of the acetic acid in quenching the OH radical initiated chain decomposition of ozone.

Table III contains the experimental results for resist removal of a 10-minute process with ozonated DI water when minor amounts of acetic acid are added to the solution. The resist removal is recalculated for the 10 min process time and is expressed as a removal rate (in nm/min). It is

worth noting that due to the experimental set-up, the measured ozone concentrations are purely qualitative (separation between ozone sensor and O_2/O_3 gas flow is not always reproducible). Adding between 0.02 mmol/l and 0.24 mmol/l of acetic acid to ozonated DI water, improves the resist removal efficiency by almost 50% compared to the unspiked reference process. The combined effect of acetic acid and hydrogen peroxide spiking is evaluated for resist removal purposes and shown in Table IV. In these runs, the DI water is initially spiked with 0.02 mmol/l of acetic acid, after ozone saturation, a variable concentration of hydrogen peroxide is added, and the effect on resist removal efficiency is evaluated. Adding of hydrogen peroxide in the presence of the acetic acid reduces the resist removal rate, though with far less strong consequences compared to the effect as seen in Table II. Also, it can be seen that the stabilizing effect induced from adding the acetic acid is stronger than observed for acidifying the solution (Table II, with HNO_3).

Higher ozone concentrations are achieved in DI water from the addition of acetic acid. However, the improvement in resist removal efficiency can not solely be explained from the increased ozone concentration upon addition of acetic acid. Figure 9 plotted impact of acetic acid addition on the resist removal process efficiency number, which is normalized for the ozone concentration. The process efficiency was seen to increase upon acetic acid addition. Therefore some other unknown mechanism is coming into play.

The organic material is confined in a layer at the silicon surface, rather than homogeneously dispersed in the solution as is the case for e.g. waste water treatment. Given the small lifetime of dissolved ozone ($t_{1/2} = 20$ min at

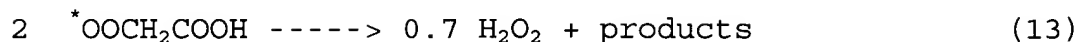
room temperature) and reactive ozone species, transfer of waste water ozone knowledge is not feasible for our applications. For good organic removal, sufficient chemical activity (reactive O_3 availability) in the vicinity of the confined layer of organic material near the wafer surface is required. It has been seen that the removal efficiency of organic contamination on silicon wafers is strongly influenced by temperature, ozone concentration and addition of acetic acid. Temperature and ozone concentration requirements are met in the moist ozone gas phase experiment described above. By exposing the wafer to a moist atmosphere, a thin condensation layer is formed on the wafer surface. Due to the ozone gas phase ambient, a continuous supply of ozone compounds through the thin condensation layer, towards the organic contamination at the silicon surface, is maintained. Also in the bubble experiment, ozone containing bubbles continuously contact the confined layer of organic contamination.

However, the critical parameter as far as ozone concentration is concerned, is not solely the total amount of 'ozone' that is available in the solution. It rather is the chemical activity that emerges in the vicinity of the confined layer of organic material near the wafer surface. In order for any ozone oxidation process to be successful, one should not necessary maximize the amount of ozone, but improve the transfer efficiency (or availability) of the ozone (molecular and radical) towards the organic contamination to be removed. The latter is likely achieved additionally from acetic acid addition.

Scavenging of OH radicals in oxygenated acetic acid solution leads to the formation of H_2O_2 via reactions described hereunder [K.Sehested et.al, Environ.Sci.Technol. 25, 1589, 1991, which is hereby incorporated by reference].



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The other products formed in reaction (13) are formaldehyde, glyoxylic acid, glycolic acid and organic
10 peroxides.

A reaction of the acetic free radical (reaction (11)), with the resist surface, might make the latter more reactive towards ozone. This could involve abstraction of an hydrogen atom, and formation of an unsaturated bond. This
15 unsaturated bond would then be available for reaction with molecular ozone. Secondly, scavenging of free OH radicals very close to the resist surface. The resulting decomposition of acetic acid according to reactions (11-13) results in the formation of e.g. H_2O_2 . Which in its turn could initiate the
20 formation of controlled and localized 'advanced oxidation power' (OH radicals) very near to the resist surface.

From the foregoing detailed description, it will be appreciated that numerous changes and modifications
25 can be made to the aspects of the invention without departure from the true spirit and scope of the invention. This true spirit and scope of the invention is defined by the appended claims, to be interpreted in light of the foregoing specification.

Table I: Designed experiment settings and results.

HAC ml	H ₂ O ₂ Ml	pH	O ₂ flow	Temp.	Pos_er nm/min	Neg_er nm/min	[O3]av ppm
1	0	5	hi	40	51.2	7.36	18.2
1	0.2	2	lo	21	34.8	3.11	54.6
1	0.1	5	hi	40	40.1	5.97	17.2
1	0	5	lo	21	36.9	2.60	52.6
0	0.2	2	lo	40	19.3	0.02	14.5
1	0	2	hi	21	36.1	2.73	44.8
0	0.2	5	lo	21	3.4	0.39	14.7
0.5	0	5	hi	40	36.3	5.91	17.1
0	0.2	5	hi	40	4.6	1.32	5.7
1	0.2	5	lo	40	31.9	5.98	17.9
0	0	2	lo	21	33.1	1.46	47.6
0	0.2	2	hi	21	26.8	1.96	37.9
0	0	5	hi	21	27.0	2.58	39.8
0	0.1	2	hi	40	20.7	2.62	11.4
0	0	2	hi	40	31.6	3.34	15.6
1	0.2	5	hi	21	31.4	2.85	44.7
1	0.2	2	hi	40	55.9	3.78	15.9
1	0	2	lo	40	41.8	3.96	17.7
0.5	0.1	5	hi	21	36.6	3.26	42.4
0.5	0.2	5	lo	40	37.0	2.93	15.1
0.5	0.2	2	hi	40	47.3	3.22	14.4
0	0	5	lo	40	11.9	1.24	13.6
1	0.1	2	lo	21	34.4	1.89	49.9

Table II: Effect of hydrogenperoxide on resist removal efficiency.

[O ₃] average w-ppm	H ₂ O ₂ added (ml)	HNO ₃ added (ml)	Resist removal (nm/min)
48.0	0	0	38.4
37.0	0.05	5.5	11.3
30.9	0.05	0	9.3
24.7	0.1	0	7.7
4.5	0.5	0	2.1

5 **Table III:** Effect of acetic acid on resist removal efficiency

[O ₃] average w-ppm	H ₂ O ₂ added (ml)	HAc added (ml)	Resist removal (nm/min)
48.0	0	0	38.4
49.5	0	0.1	47.1
50.0	0	1.1	51.1
54.3	1	1.1	34.2

Table IV: Effect of acetic acid and hydrogen peroxide on resist removal efficiency.

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[O ₃] average w-ppm	H ₂ O ₂ added (ml)	HAc added (ml)	Resist removal (nm/min)
49.5	0	0.1	47.1
45.6	0.1	0.1	21.9
38.6	0.2	0.1	18.1
46.0	1.5	0.1	22.3